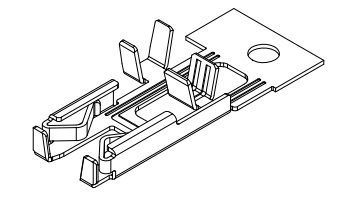
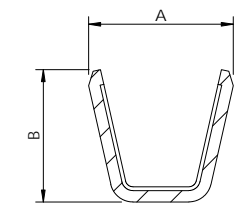
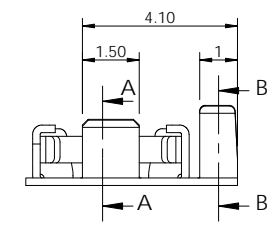
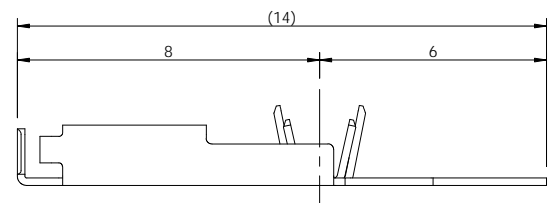
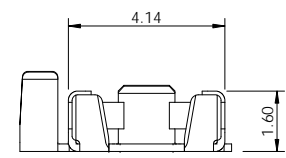
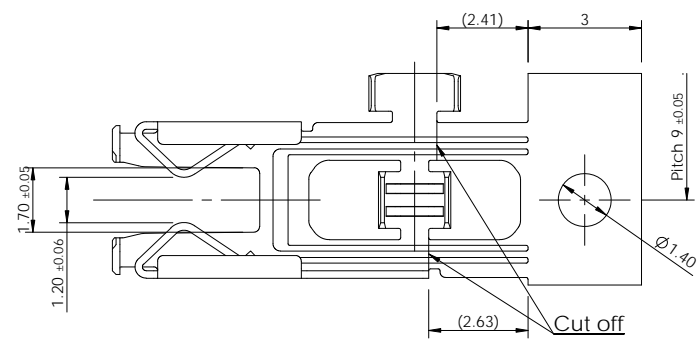


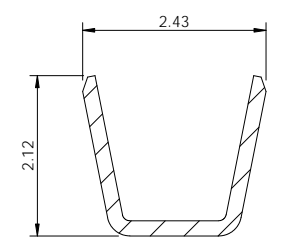
REV	REVISION RECORD	DATE	DRWN	CHKD	APVD
A	RELEASED (RD-2022-030)	07 MAR '22	JK	SJ	CY
B	CHANGED (RD-2022-149)	07 DEC '22	JK	SJ	CY



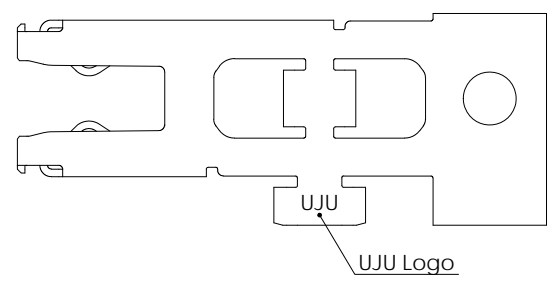
3D View



Section A-A
Scale 10 : 1



Section B-B
Scale 10 : 1



-Note-

1. Material : Copper alloy
2. General Tolerance : ±0.20
3. Finish : Pre-Sn 0.8~1.5µm over Cu 0.05~0.15µm

0.83	1.40 "F"	1.70	1.70 "F"	1.91	1.75	Ø1.2 - Ø1.5	0.3 - 0.35SQ	PCB_Slot_F_V_TML_M	KH2000008-21	△
0.65	1.25 "F"	1.60	1.70 "F"	1.62	1.13	Ø1.0 - Ø1.1	0.22 SQ	PCB_Slot_F_V_TML_S	KH2000008-20	
CCH	CCW	ICH	ICW	A	B	Insulation Range	Wire Range	Description	Part No.	Finish

TOLERANCES UNLESS OTHERWISE SPECIFIED 0 ± 0.0 ± 0.00 ± 0.000 ± 0.0000 ± ANGLES ± PRODUCT SPEC	UNIT MM THIRD ANGLE PROJECTION	DRWN JK. HWANG	(주)우주 일렉트로닉스 UJU Electronics co.,Ltd.
		CHKD SJ. LEE	
		APVD CY. CHOI	
		MATERIAL SEE NOTE	
FINISH/COLOR SEE NOTE	NAME PCB_Slot_F_V_TML		
APPLICATION SPEC	WEIGHT	CUSTOMER DRAWING	DRAWING NO KH2000008
		LOC RESTRICTED TO	SIZE A3 SCALE 5:1 SHEET 1 of 1 REV B